

L Number	Hits	Search Text	DB	Time stamp
1	107373	bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)	USPAT; IBM_TDB	2004/01/13 12:13
2	102372	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1))	USPAT; IBM_TDB	2004/01/13 12:14
3	96984	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1)	USPAT; IBM_TDB	2004/01/13 12:16
4	1417	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and card\$1	USPAT; IBM_TDB	2004/01/13 12:16
5	28	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and (vir\$4 near signature\$1)	USPAT; IBM_TDB	2004/01/13 12:55
6	146	server near farm\$1	USPAT; IBM_TDB	2004/01/13 12:18
9	2	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and (vir\$4 near signature\$1) and (enterprise\$1 isp (internet near2 service near2 provider\$1))	USPAT; IBM_TDB	2004/01/13 12:55
10	4873	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and (enterprise\$1 isp (internet near2 service near2 provider\$1))	USPAT; IBM_TDB	2004/01/13 12:47
11	2	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and (enterprise\$1 isp (internet near2 service near2 provider\$1))) and (vir\$4 near signature\$1)	USPAT; IBM_TDB	2004/01/13 12:48
12	54	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and (vir\$4 near3 signature\$1)	USPAT; IBM_TDB	2004/01/13 12:55
14	4	((bus same (cpu processor\$1 microprocessor\$1 microcontroller\$1 controller\$1)) and (input/output input output i/o port\$1)) and (memory instruction\$1 eeprom eeprom rom ram storage\$1) and (vir\$4 near3 signature\$1) and (enterprise\$1 isp (internet near2 service near2 provider\$1))	USPAT; IBM_TDB	2004/01/13 12:56
15	3	("6381698") or ("6226749") or ("5802275")).PN.	USPAT; IBM_TDB	2004/01/13 12:57
-	9675	(authentica\$3 authoriz\$4 authoris\$4) near3 (data message engine\$1 file\$1 document\$1 information\$1)	USPAT; IBM_TDB	2003/12/22 16:54
-	68132	(encrypt\$3 encipher\$4 cryptograph\$6 encod\$3) near3 (data message engine\$1 file\$1 document\$1 information\$1)	USPAT; IBM_TDB	2003/12/22 16:56

-	1523	((authenticat\$3 authoriz\$4 authoris\$4) near3 (data message engine\$1 file\$1 document\$1 information\$1)) with ((encrypt\$3 encipher\$4 cryptograph\$6 encod\$3) near3 (data message engine\$1 file\$1 document\$1 information\$1))	USPAT; IBM_TDB	2003/12/22 16:57
-	198	((authenticat\$3 authoriz\$4 authoris\$4) near3 (data message engine\$1 file\$1 document\$1 information\$1)) with ((encrypt\$3 encipher\$4 cryptograph\$6 encod\$3) near3 (data message engine\$1 file\$1 document\$1 information\$1))) and multiplex\$3	USPAT; IBM_TDB	2003/12/22 16:57
-	133	((authenticat\$3 authoriz\$4 authoris\$4) near3 (data message engine\$1 file\$1 document\$1 information\$1)) with ((encrypt\$3 encipher\$4 cryptograph\$6 encod\$3) near3 (data message engine\$1 file\$1 document\$1 information\$1))) and multiplex\$3) and serial\$3	USPAT; IBM_TDB	2003/12/22 16:58